



DEPARTMENT OF ELECTRICAL ENGINEERING &
ELECTRONICS ENGINEERING
MATS SCHOOL OF ENGG & IT
MATS UNIVERSITY, GULLU, ARANG, C.G.



School of Engineering & IT MATS University Raipur



Syllabus Scheme
(1st to 4th Semester)
For
Master of Technology
In
Power Electronics
(DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING)



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This Course aims at training graduate engineers in the field of Power Electronics. This course deals with the state of the art techniques in system-level modeling, analysis, design and integration of motor drives. The course also covers advanced topics in microprocessors and micro controllers which are very much needed for today's Power Electronics engineer. Projects of practical relevance in these areas of carried out in the final year of the course.

Program Objectives and Outcomes

PROGRAMME EDUCATIONAL OBJECTIVES

The major objectives of the M.Tech. Programme in Power Electronics are to equip the students with adequate knowledge and skills in Power Electronics and to prepare them for the following career options:

1. research programmes in Power Electronics and related areas
2. employment in R & D organisations related to sustainable technologies
3. to work in power electronic circuit design and fabrication industries
4. faculty positions in reputed institutions

Programme Outcomes for Power Electronics

A student who has undergone M.Tech. programme in Power Electronics (PE) will

1. have an ability to evaluate and analyses problems related to Power Electronic Systems and incorporate the principles in the state of art systems for further improvement
2. be able to investigate critical PE problems and to arrive at possible solutions independently, by applying theoretical and practical considerations
3. be able to solve PE problems such as switching control, converter design, analysis and control of solid state drives and stability studies
4. be able to develop appropriate power converters for sustainable energy technologies
5. be able to identify optimal solutions for improvising power conversion and transfer capability, enhancing power quality and reliability through PE based solutions
6. be able to evolve new power electronic topologies and control schemes based on literature survey and propose solutions through appropriate research



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methodologies, techniques and tools, and also by designing and conducting experiments

7. be able to work on small, well-defined projects with particular goals to provide real time solutions pertaining to power electronics
8. be able to develop, choose, learn and apply appropriate techniques, various resources including sophisticated digital controllers and IT tools for modern power electronic system simulation, including prediction and modelling with existing constraints
9. be able to develop dedicated software for analysing and evaluating specific power electronics and control problems
10. be able to participate in collaborative-multidisciplinary engineering / research tasks and work as a team member in such tasks related to PE domain, giving due consideration to ecological and economical intricacies, and lead the team in specific areas
11. be able to confidently interact with the industrial experts for providing consultancy
12. be able to pursue challenging professional endeavors based on acquired competence and knowledge
13. be a responsible professional with intellectual integrity, code of conduct and ethics of research, being aware of the research outcomes and serve towards the sustainable development of the society
14. be capable of examining critically the outcomes of research and development independently without any external drive.



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Scheme of Teaching & Examination
M.Tech in Power Electronics
I- Semester

Semester - 1								
S. No.	Code	Subject	Periods per week			Scheme of marks		Total Credit
			L	T	P	ESE	IM	
1	MT100	Research Methodology and IPR	3		2	70	30	3
1	MT131	Power Converters	3	-	-	70	30	3
2	MT132	Microcontroller & Embedded System	3	-	-	70	30	4
3	MT133	Power Electronic Circuits	3	-	-	70	30	4
4	MT134	Industrial Control Electronics	3	-	-	70	30	3
6	MT135	Power Converters Laboratory	-	-	2	30	20	2
7	MT136	Power Electronic Circuits Laboratory	-	-	2	30	20	2
Total			15	-	4	410	190	21

L – Lecture, T – Tutorial, ESE – End Semester Examination,

P – Practical, IM – Internal Marks (Include Class Test & Teacher’s Assessments)



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5. Understanding that when IPR would take such important place in growth of individuals & nation, it is needless to emphasis the need of information about Intellectual Property Right to be promoted among students in general & engineering in particular.
6. Understand that IPR protection provides an incentive to inventors for further research work and investment in R & D, which leads to creation of new and better products, and in turn brings about, economic growth and social benefits.



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4. The 8051 Microcontroller and Embedded Systems using Assembly and C, Mazidi, Mazidi & McKinlay, 2nd Ed.,PHI.



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**Scheme of Teaching & Examination
M.Tech in Power Electronics
II- Semester**

Semester - 2							
Code	Subject	Periods per week			Scheme of marks		Total Credit
		L	T	P	ESE	IM	
MT230	Switched mode Power Conversion	3	-	-	70	30	3
MT231	Power Electronics Drivers	3	-	-	70	30	3
MT232	PWM converters & Application	3	-	-	70	30	3
MT233	Advance Digital Signal Processing	3	-	-	70	30	3
MT234	Power Modules Laboratory	-	-	2	30	20	1
MT235	Power Electronics Drives Laboratory	-	-	2	30	20	1
MTP23XX	Professional Elective - 1	3			70	30	3
Total		15		4	410	190	17

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Semester : 2nd M.Tech. Course Branch : Power Electronics
Subject : PWM converters & Application
Code : MT232

UNIT :I

AC/DC and DC/AC power conversion, overview of applications of voltage source converters, pulse modulation techniques for bridge converters.

UNIT :II

Bus clamping PWM, space vector based PWM, advanced PWM techniques, practical devices in converter; calculation of switching and conduction losses.

UNIT :III

Compensation for dead time and DC voltage regulation; dynamic model of a PWM converter, multilevel converters; constant V/F induction motor drives.

UNIT :IV

Estimation of current ripple and torque ripple in inverter fed drives; line – side converters with power factor compensation.

UNIT :V

Active power filtering, reactive power compensation; harmonic current compensation.

Text Books:

1. Mohan, Undeland and Robbins," Power Electronics; Converters, Applications and Design", John Wiley and Sons, 2nd edition , 1995.
2. Erickson R W, " Fundamentals of Power Electronics", Chapman and Hall, 2001.
3. Vithyathil J, "Power Electronics: Principles and Applications ,, McGraw Hill, 1995



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Subject : Power Modules Laboratory
Code : MT234

List of Experiments:

1. Development of various configurations of power modules using SCRs, IGBTs, power transistors and power MOSFETs. Practical converter design considerations- Snubber design, gate and base drive circuits.
2. DC to DC converters of various configurations using SCRs, IGBTs, power transistors and power MOSFETs.
3. DC to AC converters of various configurations using SCRs, IGBTs, power transistors and power MOSFETs.
4. AC to AC converters of various configurations using SCRs, IGBTs, power transistors and power MOSFETs..
5. Practical implementation of control techniques for voltage control, speed control and harmonic minimization.



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Subject : Power Electronics Drives
Laboratory

Code : MT235

List of Experiments:

1. Micro controller based speed control of Converter/Chopper fed DC motor.
2. Micro controller based speed control of VSI fed three-phase induction motor.
3. Micro controller based speed control of Stepper motor.
4. DSP based speed control of BLDC motor.
5. DSP based speed control of SRM motor.
6. Self-control operation of Synchronous motors.
7. Condition monitoring of three-phase induction motor under fault conditions.
8. Re-programmable Logic Devices and Programming
 - (a) VHDL programming – Examples
 - (b) Verilog HDL programming – Examples
 - (c) Realization of control logic for electric motors using FPGA.
9. Simulation of Four quadrant operation of three-phase induction motor.
10. Simulation of Automatic Voltage Regulation of three-phase Synchronous Generator.
11. Design of switched mode power supplies



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**Scheme of Teaching & Examination
M.Tech in Power Electronics
III- Semester**

Semester - 3							
Code	Subject	Periods per week			Scheme of marks		Total Credit
		L	T	P	ESE	IM	
MTP3XX	Professional Elective - 2	3	-	-	70	30	3
MTP3XmX m	Professional Elective - 3 - Through MOOC	3	-	-	70	30	3
MT332	Renewable Energy Sources Laboratory	-	-	2	30	20	2
MT333	Project Work Phase - I	-	-	-	140	60	10
	Total	6		2	310	140	18

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Semester - 4							
Code	Subject	Periods per week			Scheme of marks		Total Credit
		L	T	P	ESE	IM	
MT440	Project Work Phase - II + Seminar	-	-	-	315	135	16
Total		-	-	-	315	135	16



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Semester : 2nd and 3rd M.Tech. Course Branch : Power Electronics
Subject : **PROFESSIONAL ELECTIVE-I & II**
Code : MTP3XX

Code	PROFESSIONAL ELECTIVE-I
MTP301	Advance Control Theory
MTP302	Power Systems Operation And Control
MTP303	Energy Auditing, Conservation & Management
MTP304	Advanced Power System Protection
MTP305	Transient Over Voltages In Power Systems
MTP306	Artificial Neural Networks
MTP307	Optimization Techniques
MTP308	HVDC Transmission
MTP309	Computer Aided Design Of Power Electronic Circuits
MTP2310	Power System Planning and Reliability
MTP311	Static VAR Control & Harmonic Filtering
MTP312	Flexible AC Transmission Systems
MTP313	Digital Controllers in Power Electronics Applications
MTP314	Power Quality
MTP315	Programmable Logic Controllers And Their Applications
MTP316	Fuzzy Systems
MTP317	Digital Simulation of Power Electronic Systems
MTP318	Electrical Energy Conservation and Management
MTP319	Renewable Energy Sources
MTP320	Machine Modelling And Analysis



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New York, 1998.

Reference:

1. Power System Protection, PM Anderson, IEEE Press Book
2. Protective Relays Application and Guide, GEC Measurements
3. Jones D., " Analysis and protection of electrical power systems", Pitman Publishing, 1971

Semester :	M.Tech. Course	Branch :	Power Electronics
Subject :	Transient Over Voltages In Power Systems	Total Theory Periods :	40
	Code :		MTP305

Unit-I

Introduction and survey: Review of various types of power system transients – effect of transients on power systems –relevance of the study and computation of power system transients.

Unit-II

Lighting surges: Electrification of thunderclouds – lightning current surges – lightning current parameters and their values – stroke to tower and midspan – induced lightning surges.

Unit-III

Switching surges: Closing and reclosing of lines – load rejection – fault initiation – fault clearing – short line faults – Ferro – resonance – isolator switching surges – temporary over voltages – surge on an integrated system – switching – harmonics.

Unit-IV

Computation of transient in conversion equipment: Travelling wave method – Beweley’s Lattice diagram – analysis in time and frequency domain – eigen value approach – Z-transform – EMTP software.

Unit-V

Insulation coordination: Over voltage protective devices – shielding wires, rods gaps and surge diverters, principles of insulation co ordination-recent advancements in insulation co ordination – design of EHV system.

References:

1. Allan Greenwood, Electrical transients in Power Systems, Wiley Interscience, New York, 1971.
2. Klaus Ragaller, Surges in High Voltage Networks, Plenum Press, New York, 1980.
3. Diesendorf W., Over Voltages On High Voltage Systems, Renselaer Bookstore, Troy New York, 1971.
4. Peterson H.A., transients in power systems, Dover Publications, New York, 1963.

